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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

NONE JAT

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-192670 06/26/2001

YES JAT

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 11	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>[Signature]</i> Initials JAT			
Verified and Acknowledged				

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## TITLE

Method of performing bit modeling and circuit that uses this method

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